

Course Code	TKIT163103													
Course Name	Interfaces and Peripherals													
Course Instructors	Dani Adhipta													
Course Type	Required													
Course Classification	Engineering Topics													
Credit / Contact Hour per Week	2 / 100 minutes per Week													
Course Description	This course deals with trends, theories and concepts, the implementation along with the utilization of peripheral devices and interfaces in information technology. The working principle of input and output (Input / Output, I / O) and electronic hardware are discussed in class													
Prerequisites Courses	-													
<b>Covered Student Outcome</b>	<b>Development of Engineering Solution (b)</b> <b>Engineering Design (c)</b> <b>Modern Tools Utilization (e)</b>													
Learning Outcome	<ol style="list-style-type: none"> <li>1. Students are able to understand the latest trends and development of interface and peripheral technologies</li> <li>2. Students are able to identify peripheral devices and peripheral functions and computer interface devices</li> <li>3. Students are able to design simple peripheral devices and their interfaces</li> <li>4. Students understand the strategy of utilizing peripheral devices and their interfaces in business</li> </ol>													
Topic	<ol style="list-style-type: none"> <li>1. Introduction (trend) Interfaces and Peripherals</li> <li>2. Definitions, examples, and Standard interfaces</li> <li>3. Types of interfaces and peripherals</li> <li>4. Modern computer architecture in general</li> <li>5. Parallel / serial, synchronous / asynchronous data communications</li> <li>6. Concepts and theories of interfaces and peripherals</li> <li>7. Timing and buffering</li> <li>8. Hardware I / O electronics, bus type</li> <li>9. Utilization in business</li> </ol>													
<b>Direct Assessment</b>	<table border="1"> <thead> <tr> <th><b>Direct Assessment Plan</b></th> <th><b>Measured Learning Outcome</b></th> </tr> </thead> <tbody> <tr> <td>Mid Exam</td> <td>LO1, LO2</td> </tr> <tr> <td>Final Exam</td> <td>LO3, LO4</td> </tr> <tr> <td>Homework</td> <td></td> </tr> <tr> <td>Presentation</td> <td></td> </tr> <tr> <td></td> <td></td> </tr> </tbody> </table>		<b>Direct Assessment Plan</b>	<b>Measured Learning Outcome</b>	Mid Exam	LO1, LO2	Final Exam	LO3, LO4	Homework		Presentation			
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Mid Exam	LO1, LO2													
Final Exam	LO3, LO4													
Homework														
Presentation														
Indirect Assesment	Questionnaire and direct communication													
References	<ol style="list-style-type: none"> <li>1. Computer System Architecture</li> <li>2. SPI Microcontroller Manual Technical Book (Intel, Atmel, dll)</li> <li>3. I2C standard manual guide</li> </ol>													